

FORM PTO-1392 (REV 11-2000)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NUMBER 124-884	
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371				U.S. APPLICATION NO. (If known, see 37 C.F.R. 1.5) 09/914920	
INTERNATIONAL APPLICATION NO. PCT/GB00/00753		INTERNATIONAL FILING DATE 3 March 2000		PRIORITY DATE CLAIMED 6 March 1999	
TITLE OF INVENTION IMPROVEMENTS RELATING TO ANNEALING					
APPLICANT(S) FOR DO/EO/US DONOHUE et al					
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:					
1. <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371. 2. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371. 3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)). The submission must include items (5), (6), (9) and (21) indicated below. 4. <input type="checkbox"/> The U.S. has been elected by the expiration of 19 months from the priority date (Article 31). 5. A copy of the International Application as filed (35 U.S.C. 371(c)(2)). a. <input type="checkbox"/> is attached hereto (required only if not communicated by the International Bureau). b. <input checked="" type="checkbox"/> has been communicated by the International Bureau. c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). 6. <input type="checkbox"/> An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)). a. <input type="checkbox"/> is attached hereto. b. <input type="checkbox"/> has been previously submitted under 35 U.S.C. 154(d)(4). 7. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)). a. <input type="checkbox"/> are attached hereto (required only if not communicated by the International Bureau). b. <input type="checkbox"/> have been communicated by the International Bureau. c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired. d. <input type="checkbox"/> have not been made and will not be made. 8. <input type="checkbox"/> An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)). 9. <input type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)). 10. <input type="checkbox"/> A English language translation of the annexes of the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)). Items 11 To 20 below concern document(s) or information included: 11. <input checked="" type="checkbox"/> An Information Disclosure Statement under 37 C.F.R. 1.97 and 1.98. 12. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 C.F.R. 3.28 and 3.31 is included. 13. <input checked="" type="checkbox"/> A FIRST preliminary amendment. 14. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. 15. <input type="checkbox"/> A substitute specification. 16. <input type="checkbox"/> A change of power of attorney and/or address letter. 17. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821-1.825. 18. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4). 19. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4). 20. <input checked="" type="checkbox"/> Other items or information. PTO-1449 Form and International Search Report					

U.S. APPLICATION NO. (If known, see 37 C.F.R. 1.5) <div style="font-size: 2em; font-weight: bold; margin-top: 5px;">09/914920</div>		INTERNATIONAL APPLICATION NO. PCT/GB00/00753		ATTORNEY'S DOCKET NUMBER 124-884	
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21. <input checked="" type="checkbox"/> The following fees are submitted: BASIC NATIONAL FEE (37 C.F.R. 1.492(a)(1)-(5): -- Neither international preliminary examination fee (37 C.F.R. 1.482) nor international search fee (37 C.F.R. 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO\$1000.00 -- International preliminary examination fee (37 C.F.R. 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO.....\$860.00 -- International preliminary examination fee (37 C.F.R. 1.482) not paid to USPTO but international search fee (37 C.F.R. 1.445(a)(2)) paid to USPTO.....\$710.00 -- International preliminary examination fee (37 C.F.R. 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4).....\$690.00 -- International preliminary examination fee (37 C.F.R. 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4).....\$100.00 <div style="text-align: right; margin-right: 50px;">ENTER APPROPRIATE BASIC FEE AMOUNT =</div> Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input checked="" type="checkbox"/> 30 months from the earliest claimed priority date (37 C.F.R. 1.492(e)).				CALCULATIONS PTO USE ONLY		
				\$	860.00	
				\$	130.00	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE			
Total Claims	22	-20 =	2	X	\$18.00	\$ 36.00
Independent Claims	2	-3 =	0	X	\$80.00	0.00
MULTIPLE DEPENDENT CLAIMS(S) (if applicable)					\$270.00	\$ 0.00
TOTAL OF ABOVE CALCULATIONS =						\$ 1026.00
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.						0.00
SUBTOTAL =						\$ 1026.00
Processing fee of \$130.00, for furnishing the English Translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 C.F.R. 1.492(f)).					+	0.00
TOTAL NATIONAL FEE =						\$ 1026.00
Fee for recording the enclosed assignment (37 C.F.R. 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 C.F.R. 3.28, 3.31). \$40.00 per property					+	\$ 0.00
Fee for Petition to Revive Unintentionally Abandoned Application (\$1240.00 - Small Entity = \$620.00)						\$ 0.00
TOTAL FEES ENCLOSED =						\$ 1026.00
				Amount to be:		
				refunded		\$
				Charged		\$

a. <input checked="" type="checkbox"/> A check in the amount of \$1026 to cover the above fees is enclosed. b. <input type="checkbox"/> Please charge my Deposit Account No. 14-1140 in the amount of \$_____ to cover the above fees. A duplicate copy of this form is enclosed. c. <input checked="" type="checkbox"/> The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 14-1140. A duplicate copy of this form is enclosed. d. <input checked="" type="checkbox"/> The entire content of the foreign application(s), referred to in this application is/are hereby incorporated by reference in this application.	<div style="text-align: center; margin-top: 50px;"> </div> <div style="text-align: center;"> Stanley C. Spooner NAME </div> <div style="text-align: center; margin-top: 20px;"> 27,393 REGISTRATION NUMBER </div> <div style="text-align: center; margin-top: 20px;"> September 6, 2001 Date </div>
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NOTE: Where an appropriate time limit under 37 C.F.R. 1.494 or 1.495 has not been met, a petition to revive (37 C.F.R. 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

DONOHUE et al

Atty. Ref.: 124-884

National Phase of Int'l Appln. No. PCT/GB00/00753 Group:

(Filed: 3 March 2000)

Examiner:

For: IMPROVEMENTS RELATING TO ANNEALING

* * * * *

September 6, 2001

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

PRELIMINARY AMENDMENT

Please amend the above-identified application as follows:

IN THE CLAIMS

Cancel claims 1-23 without prejudice and add the following new claims in lieu thereof.

24. (New) A method of producing an integrated ferroelectric device comprising a first layer of material capable of existing in a ferroelectric state and a second layer of material defining an integrated circuit, said method comprising the steps of:

producing a pulse of energy having a first temporal width;

extending said first temporal width of said pulse by passing said pulse through a temporal extender to produce a processed pulse having a greater temporal width than said first temporal width; and

illuminating said first layer with said processed pulse to convert some or all of said material in said first layer from a non-ferroelectric state into a phase capable of exhibiting ferroelectricity or otherwise improving the quality of said material of said first layer without exceeding the temperature budget of said integrated circuit of said second layer.

25. (New) A method to according claim 24 wherein said method further comprises generating more than one of said processed pulses and sequentially illuminating said first layer with said processed pulses.

26. (New) A method according to claim 24 wherein said material of said first layer comprises a low grade deposited perovskite and said method improves the quality of said perovskite material.

27. (New) A method according to claim 24 wherein said first layer includes material deposited in a non-perovskite phase and said method converts some or all of the material into said perovskite phase.

28. (New) A method according to claim 24 wherein said pulse of energy comprises a pulse of energy produced using a laser.
29. (New) A method according to claim 28 wherein said pulse produced by said laser has a temporal length between 10ns and 25ns.
30. (New) A method according to claim 1 wherein said temporal extender increases the temporal length of said pulse to produce a processed pulse with a temporal length of approximately 300ns, or between substantially 300ns and 400ns, or longer.
31. (New) A method according to claim 24 wherein said processed pulse comprises more than one sub-pulse, each of said sub-pulses corresponding to a pulse action of said extender.
32. (New) A method according to claim 24 wherein said processed pulse has a fluence and temporal width that is compatible with properties of said material of said first layer such that the temperature throughout said layer (or over a substantial depth of said first layer) exceeds a predetermined anneal temperature whilst the temperature of said second layer is within said temperature budget of said circuitry.

33. (New) A method according to claim 24 wherein said first layer comprises a top layer of said device.

34. (New) A method according to claim 24 wherein two different sources of energy are produced, each source producing a respective pulse and at least one of said respective pulses being extended by a pulse extender to produce a processed pulse, and wherein said first layer is illuminated by both pulses.

35. (New) A method according to claim 34 wherein said layer is illuminated by both pulses substantially simultaneously.

36. (New) A method according to claim 34 which further comprises providing a metallic layer between said first layer and said second layer and illuminating said first layer with the two different processed pulses.

37. (New) A method according to claim 24 wherein said first layer is illuminated with said processed pulse whilst said ambient temperature of said device is maintained higher than room temperature.

38. (New) An apparatus for producing an integrated ferroelectric device, said device comprising at least a first layer of material capable of existing in a perovskite

phase and second layer of material defining an integrated circuit, said apparatus comprising:

pulse generating means for generating a pulse of energy having a first temporal width;

pulse extending means for extending said first temporal pulse width of said pulse to provide a processed pulse of greater temporal width;

and guide means for guiding said processed pulse of energy onto said first layer.

39. (New) Apparatus according to claim 38 which further comprises depositing means for depositing said first layer of material above said second layer in which some or all of said first layer is in a non-perovskite phase.

40. (New) Apparatus according to claim 38 wherein said pulse generating means comprises a laser.

41. (New) Apparatus according to claim 40 wherein said laser has a wavelength in the ultraviolet spectrum.

42. (New) Apparatus according to claim 38 wherein said depositing means is adapted to deposit a first layer of material above said second layer after one or more intermediate layers are deposited onto said second layer.

43. (New) Apparatus according to claim 42 wherein one of said intermediate layers comprises a sacrificial layer.

44. (New) Apparatus according to claim 38 wherein said pulse extender is adapted to increase said temporal pulse width of said first pulse by substantially at least two times.

45. (New) Apparatus according to claim 38 wherein said pulse extender is adapted to produce a processed pulse that comprises a plurality of sub-pulses.

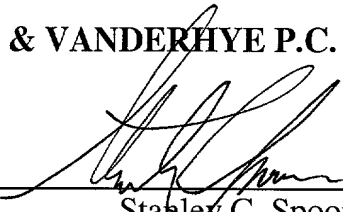
REMARKS

Entry of the foregoing amendments is requested.

Respectfully submitted,

NIXON & VANDERHYTE P.C.

By: _____


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09/914928

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IMPROVEMENTS RELATING TO ANNEALING

This invention relates to improvements in annealing, and in particular to a method and apparatus for annealing ferroelectric thin film materials.

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There has been a considerable amount of research into the development of devices which utilise the thermal properties of ferroelectric materials. One example is the development of infrared imaging cameras based on two-dimensional arrays of ferroelectric thermal detectors which are attractive due to their near ambient temperature operation. Thermal detectors used for infra-red imaging rely on the temperature change of the sensing material due to absorption of infra-red radiation. With ferroelectric materials this radiation causes a change in the electrical polarisation of the material which enables the magnitude of the change in temperature to be detected.

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In order to reduce the size of the detectors, combined integrated ferroelectric devices have been developed in which the ferroelectric material is combined with the electronic read out circuitry in a single device. Typically, these devices comprise layered structures with a thin layer of ferroelectric sputtered or spin coated or otherwise deposited onto or above one or more base layers. Other examples of such integrated ferroelectric devices are thin film piezoelectric actuators and ferroelectric random access memories (FeRAM).

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The combination of the ferroelectric material with the active circuitry in one package produces a more compact device than the provision of a separate read out circuit and improves yield, reduces cost and improves performance. However, a fundamental problem with such devices is the need to deposit the ferroelectric material within a thermal budget that is

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compatible with the integrated circuitry not being damaged or destroyed by elevated temperatures. It is widely recognised that exposure of an integrated circuit to temperatures above 450°C is a constraint on the processing of chips/materials with IC content, and this conflicts with the growth requirements of many ferroelectric layers.

A particularly important family of ferroelectric materials in use and under investigation for IR detector, actuator or FeRAM applications is the perovskites. This family include materials such as lead scandium tantalate (PST), lead zirconate titanate (PZT), barium strontium titanate (BST), lead titanate (PT) and others. For use as a ferroelectric the material layer must be in the perovskite phase. It can either be deposited directly into that phase at an elevated temperature or at a lower temperature which is then subsequently annealed into the ferroelectric perovskite phase. Layers deposited at low temperatures are generally in an amorphous, pyrochlore or other phase which is incapable of exhibiting ferroelectricity. For PST, for example, the material must be deposited at temperatures in excess of 450°C to enter the perovskite phase. Direct depositing of these materials in a perovskite phase is therefore incompatible with the temperature budgets of integrated circuitry.

One known way of providing a layer of ferroelectric material in the perovskite phase without damaging an ROIC provided on a base layer is to deposit the material in a non-ferroelectric state at a low temperature (say less than 450°C). The material may then be annealed using a laser to heat the layer sufficiently to convert the material into its perovskite phase.

In order to understand the effect of laser annealing, consider the structure of a typical uncooled microbridge type IR detector. From the top surface

downwards prior to the electrode deposition, the layers of material are as given in Table 1.

Material	Thickness	Purpose
PST	1 μ m	Ferroelectric
Pt	1000Å	Bottom electrode
Ti	50Å	Adhesion layer
SiO ₂	1000Å	Barrier layer
Sac	1-2.5 μ m	Sacrificial layer
SiO ₂	0.8 μ m	Passivation/barrier layer
ROIC		Active circuitry
Si	500 μ m	Substrate wafer

Table 1

In order to heat the PST layer sufficiently without damaging the ROIC (Read Out Integrated Circuitry) layer, the laser wavelength must be chosen so that strong absorption occurs in the PST layer. The temporal width of the pulse must also be kept sufficiently short that the heat diffusion length is small enough to prevent the induced heat wave from penetrating through the layers to the ROIC layer. For relatively thin layers up to, say, 1000Å these criteria are satisfied by commercially available excimer lasers. These lasers operate in the ultraviolet (UV) and have short pulse lengths of around 25ns. Pulses are delivered to the layer as either a single shot or at a slow repetition rate of 100's of Hz or thereabouts. The measured reflectivity of low temperature deposited (non-perovskite) PST at the wavelength of a typical commercial excimer laser of 248nm is 21%. This indicates strong absorption, whilst the absorption length at this wavelength, calculated from experimental data, is 19nm indicating strong surface absorption.

- To generate sufficient temperature at the bottom of a thick layer of PST (greater than 1000Å) requires relatively high energy density excimer lasers. Increasing the power increases the surface temperature. This places a limit on the maximum possible thickness of PST which can be annealed at around 1000Å due to extreme surface heating which can cause surface damage, poor crystallisation and crystal quality, poor film physical integrity and loss of stoichiometry due to evaporation of volatile components. As a result, the use of such a laser is unsatisfactory for layers above, say 2000Å (and more so for even thicker layers), with extremely high temperatures being generated at the surface of the layer and high thermal gradients being generated in the PST due to the short absorption and diffusion lengths resulting from the short pulse duration.
- 15 An object of the present invention is to overcome or alleviate the problems involved in the laser annealing of relatively thick layers of ferroelectric materials posed by the constraints of commercially available lasers.
- 20 In accordance with a first aspect, the invention provides a method of producing an integrated ferroelectric device comprising a first layer of material capable of existing in a ferroelectric state and a second layer of material defining an integrated circuit, the method comprising the steps of:
- 25 producing a pulse of energy having a first temporal width;
- extending the temporal width of said pulse by passing it through a temporal extender to produce a processed pulse having a greater temporal width; and
- 30

illuminating the first layer with said processed pulse to convert some or all of the material in the first layer from a non-ferroelectric state into a phase capable of exhibiting ferroelectricity or otherwise improving the quality of the material of the first layer without exceeding the temperature budget of the integrated circuit of said second layer.

The method may further comprise generating a number of such processed pulses and sequentially illuminating the device with said pulses.

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Preferably, the material of the first layer comprises a low grade deposited perovskite and the method improves the quality (i.e. greater crystal order and/or crystal size) of the perovskite material. Alternatively, the material may be deposited substantially in a non-perovskite phase and the method converts some or all of the material into the perovskite phase.

15

Preferably, the pulse of energy comprises a pulse of energy produced using a laser. More than one such pulse may be produced, with each pulse being temporally extended. The first layer may be sequentially illuminated by a number of such processed pulses.

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By providing a temporal extender, it is possible to deliver the laser energy to the first layer at a slower rate than is possible with a non-extended laser pulse using commercially available laser sources. This increases the diffusion length in the material ensuring more even heating throughout the layer and reduced surface temperatures at the face of the first layer nearest the laser source.

25

The laser pulse may be produced using an excimer laser. It may have a wavelength in the ultraviolet of around 248nm (for a KrF excimer laser). Alternatively, the pulse may be produced using a CO₂ laser.

- 5 The pulse produced by the laser may have a temporal length of substantially 10ns, or 20ns or perhaps substantially 25ns, or even beyond or any value within a range of values limited by one or more of the preceding values. This represents the limit for current commercially available excimer lasers.

10

The temporal extender may increase the temporal length of the pulse to produce a processed pulse with a temporal length of approximately 300ns, or between substantially 300ns and 400ns, or perhaps longer. For instance, the processed pulse may have a temporal length that is an order
15 of magnitude greater than the unprocessed pulse.

20

The extended pulse may comprise more than one sub-pulse, each sub-pulse corresponding to a pulse action of the extender. These may be separated by a known time interval to produce a sequence of closely spaced sub-pulses defining the processed pulse. The temporal width of each subpulse may correspond to the temporal width of the unprocessed pulse. They may be separated by, say, substantially 25ns or substantially 30ns or substantially 50ns or less or more or any range bounded by one of the values. In the case of ten sub pulses, an processed pulse with a
25 temporal width of about 400ns is produced.

The processed pulse may, therefore, comprise two, three, four, or perhaps ten or more sub-pulses which are temporally spaced close together to form a processed pulse. By close we mean that the spacing between sub-pulses

may be less than the width of each sub-pulse or perhaps equal to the sub-pulse width, or larger than the sub pulse width.

Each sub-pulse may be produced by partial reflection of the unprocessed pulse within the temporal extender.

The method may comprise producing a processed pulse having a fluence and temporal width that is compatible with the properties of the material of the first layer such that the temperature throughout the layer (or over a substantial depth of the first layer) exceeds a predetermined anneal temperature whilst the temperature of the second layer is within the temperature budget of the circuitry.

In one especially useful embodiment, the processed pulse properties (including fluence, temporal width and wavelength) may be selected so that the whole of the first layer exceeds the transition temperature for placing the material into a ferroelectric perovskite phase. This may be greater than 450°C. At the same time, the peak temperature in the second layer may be kept lower than 450°C.

20

The first layer may comprise a layer of PST (or other material) of thickness of substantially 0.1µm, or perhaps substantially 1µm, or perhaps substantially 0.8µm or substantially 1.2µm or any value therebetween.

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The first layer may comprise the top layer of the device. Alternatively, it may have other layers provided both above and below it defining a sandwich like structure. The integrated circuitry may be provided below the first layer.

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In a refinement of the method, at least one additional layer may be provided above the first layer (i.e. on the opposite side to the second layer).

- 5 Two different sources of energy may be used, each source producing a respective pulse and each respective pulse being extended by a pulse extender to produce a processed pulse. The first layer may then be illuminated by both processed pulses. This may be either substantially simultaneously or sequentially.

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The different sources of energy may comprise two different lasers, each generating a pulse of a different wavelength. For instance, one source may comprise a KrF laser or other type of excimer laser, whilst the other source comprises a carbon dioxide (CO₂) laser.

15

- In one arrangement, the method may include providing a metallic layer between the first layer and the second layer and illuminating the first layer with two different processed pulses. A CO₂ laser and a KrF laser may be used whereby two effects arise. Firstly, the first layer is heated from the top down by the KrF laser pulse. Secondly, the first layer is heated up from the bottom upwards due to heating of the metallic layer when excited by the CO₂ laser pulse. This effectively heats the layer from both sides.

- 20 from the top down by the KrF laser pulse. Secondly, the first layer is heated up from the bottom upwards due to heating of the metallic layer when excited by the CO₂ laser pulse. This effectively heats the layer from both sides.
- 25 The fluence of the pulse may be substantially be 0.05 J/cm², 0.1J/cm², 0.2J/cm² or perhaps a higher value or a lower value. It may be selected to be any value within a range bounded at its upper limit and/or lower limit by one or more of these values. For example, it may be in the range 0.05-0.1J/cm² or 0.1-0.2J/cm² or 0.05-0.1J/cm².

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The temporal width and fluence of the processed pulse may also be chosen to match the thickness and properties of the first layer materials so that the surface temperature at the first layer (i.e. the surface exposed to radiation) does not exceed a predetermined maximum temperature. Using
5 a suitable processed pulse width on a 1µm layer, it is possible to keep the surface temperature below the perovskite melting temperature of approximately 1500°C (depending on the material used).

Of course, it will be readily appreciated that instead of commencing with
10 a short pulse having a first temporal duration, and extending the pulse, a longer initial pulse could be employed by using a bespoke laser device. However, this would prove more costly and so is not preferred. We may wish to seek protection for such a method in which a non-extended pulse is used.

15 In a refinement, the first layer may be illuminated with the processed pulse whilst the ambient temperature of the device is maintained higher than room temperature. An ambient temperature in the range 100°C-450°C, or 200°C-450°C, or any other range between the limits 100°C and
20 500°C approximately could be used. An ambient temperature of 300°C is preferred. This means that it is possible to use a lower energy laser pulse source it has less work to do to raise the temperatures above the phase transition temperature. In a further refinement, the laser light could illuminate the substrate during deposition of the first layer.

25 It will also be appreciated that there may be a considerable time delay between depositing the layer (or layers) forming the device and the annealing steps. For instance, the device layers may be deposited in one factory or room within a factory before moving to another room or
30 factory to be annealed. Indeed, the method may find application in

annealing any device having first and second layers at any time in its life or before or after use.

The first layer of material may be deposited at a temperature below that
5 needed to form perovskite and may be deposited substantially wholly as
non-perovskite phase. For instance, for PST a non-perovskite layer would
be produced by depositing at below 300°C (to produce an amorphous
material) or between 300°C or at 500°C to produce a pyrochlore material.
The higher the temperature used to deposit, the more likely that some of
10 the material will exist in the perovskite phase before annealing. Of
course, the temperature used to deposit the material must not exceed the
temperature budget of the second layer.

By temperature budget, the skilled man will of course appreciate that we
15 mean the maximum temperature that the second layer can be heated to
without causing unacceptable damage or degradation to the second layer.

In accordance with a second aspect, the invention provides an apparatus
for producing an integrated ferroelectric device, said device comprising at
20 least a first layer of material capable of existing in a perovskite phase and
second layer of material defining an integrated circuit, the apparatus
comprising:

pulse generating means adapted to generate a pulse of energy having a
25 first temporal width;

pulse extending means adapted to extend the temporal pulse width of said
pulse to provide a processed pulse of greater temporal width;

and guide means adapted to guide said processed pulse of energy onto said first layer whereby some or all of the material in the first layer is converted from a non-ferroelectric state into a ferroelectric state or to otherwise improve the quality of the material of the first layer without exceeding the temperature budget of the integrated circuit of said second layer.

The apparatus may further comprise depositing means for depositing said first layer of material above said second layer in which some or all of said first layer is in a non-perovskite phase.

The pulse generating means may, for example, comprise a laser such as an excimer laser, such as a krypton fluoride (KrF) laser. Alternatively, it may comprise a carbon dioxide (CO₂) laser. The laser may have a wavelength in the ultraviolet spectrum, for instance 248nm. An example of a suitable laser is the Lambda Physik LPX210i Krf excimer laser.

The depositing means may be adapted to deposit a first layer of material above the second layer after one or more intermediate layers are deposited onto the second layer. One of these intermediate layers may comprise a sacrificial layer that is subsequently removed to leave a space between the first and second layers to form a microbridge. By providing electrical contacts between the integrated circuitry of the second layer and the first layer the device may act as an infrared thermal detector.

Removal means may therefore be provided for removing the sacrificial layer. The layer be removed before or after the first layer is annealed.

The device may comprise a thermal detector such as a pyroelectric or dielectric bolometer type infrared detector. This may comprise an array

of device defining a thermal imaging camera, perhaps an uncooled array. Alternatively, it may comprise a piezoelectric actuator or perhaps a ferroelectric random access memory. A number of devices may be provided in an array on a single wafer. Of course, in a modification it
5 will be within the scope of protection sought to provide an apparatus for annealing any device which includes a first layer that is to be annealed (perhaps not ferroelectric) and a second layer which is sensitive to overheating.

10 Where an array of devices are provided, the processed pulse may illuminate more than one or preferably all of the devices in the array simultaneously. Alternatively, the processed pulse may be applied sequentially to the devices by scanning a laser beam made up of a number of processed pulses across the array of devices. In another arrangement,
15 the laser may be fixed whilst the array of devices moves relative to the laser using one or more translation stages.

The apparatus may further include means for raising the ambient temperature of the integrated device during annealing. This may comprise
20 a heating element upon which the device is placed.

The apparatus may further comprise means for evacuating the air from around the device during annealing. For example, a vacuum chamber may be provided with the device being placed in the vacuum chamber. An
25 inlet port may be provided whereby the chamber can be filled with one or more gases, such as oxygen, during the annealing.

The pulse extender may be adapted to increase the temporal pulse width of the first pulse by substantially two times or four times, or substantially
30 ten times or more than ten times or any value therebetween. In one

arrangement, the pulse extender may be adapted to produce a processed pulse that comprises a number of sub-pulses, each sub pulse corresponding to the first pulse. This may be achieved using partial multiple reflections of the first pulse. A suitable pulse extender can be
5 obtained from Exitech Limited, Hanborough Park, Long Hanborough, Oxford, OX8 8LH.

In accordance with a third aspect, the invention provides an integrated ferroelectric device comprising at least a first layer of ferroelectric
10 material and a second layer comprising an integrated circuit, in which said first layer is transformed into a perovskite phase using a pulse of energy from a laser that has been temporally extended.

The first layer may comprise a material selected from the class of material
15 which can exist in a perovskite phase, such as PST, lead zirconate titanate (PZT), barium strontium titanate (BST), lead titanate (PT) and others.

The second layer may comprise silicon, silicon oxide and the requisite metallisation and implant doping layers to define the integrated circuit.
20 Of course, other materials are possible.

The device may comprise a microbridge. This may form a part of an imaging device to detect incident radiation. An array of devices may be provided, for example to produce a two-dimensional image of a scene. In
25 this case, the first layer may be spaced from the second layer to define a bridge overlapping the second layer.

The upper surface of the bridge may have a metal coating. The lower surface of the bridge may have a metal coating as well as or instead of the
30 upper layer. The metal coating on the lower surface of the bridge may

comprise a platinum layer overlapping a titanium layer. The metal coating on the upper surface may comprise a titanium layer.

5 The integrated circuit of the second layer may comprise a read out integrated circuit (ROIC). It may include an amplifier adapted to amplify signals from the first layer.

10 A barrier layer, such as silicon dioxide may be provided on the lower surface of the bridge. This may overlap any metallic layer that is present. The second layer may also be provided with a barrier layer. Again, this may be silicon dioxide for example. The barrier layers prevent sacrificial material which defines the gap between the first and second layers from reacting with the layers during manufacture of the device. Of course, in the finished device, substantially all of the sacrificial material may well
15 have been removed using a suitable etch. A thermal barrier layer, e.g. a layer of SiO₂ having a low thermal diffusivity may be provided below the first layer in the gap to improve the downward flow of heat away from the first layer.

20 Of course, the device does not need to comprise a microbridge. It may, for example, comprise an alternate form of infrared sensing device. It may be a thin film piezoelectric actuator or a ferroelectric random access memory (FeRAM) or a dynamic random access memory (DRAM).

25 Most preferably the thickness of the first layer of ferroelectric material is greater than 2000Å, or greater than 5000Å, or up to 1µm. Substantially all of the material throughout the thickness of the layer may exist in the perovskite phase which is annealed using the extended laser pulse.

In accordance with yet a further aspect, the invention provides a method of preferentially heating a first layer of material to a first temperature without heating a second layer of a material provided below said first layer to said first temperature by illuminating the first layer with a temporally extended pulse of radiation from a laser source.

By temporally extending a pulse, the surface temperature of the first layer when illuminated with sufficient pulse energy to heat the whole layer up to or above the first temperature is lower than for a corresponding un-extended pulse where the energy is supplied in a shorter period of time.

Of course, in an alternative the first layer may be heated to such an extent that the uppermost surface layer exceeds its melting point or is otherwise damaged. After annealing, this damaged (unwanted) layer could be milled away or otherwise removed.

The first layer may comprise a material which is capable of existing in a ferroelectric state. The second layer may comprise a silicon base upon which an integrated circuit is formed. It is generally accepted that such circuits will be damaged if heated in excess of 450°C, whilst most ferroelectric materials need to be heated in excess of 450°C to produce a high grade ferroelectric material. This can be achieved using the method of the present invention for layers of ferroelectric material up to at least 1µm in thickness.

Preferably, the pulse width is selected so that it is sufficiently long to ensure that the heat at the surface of the first layer diffuses away at a rate that keeps the surface temperature below melting, yet sufficiently long that the surface temperature of the second layer does not exceed the first

temperature. This will depend on the properties of the first layer and the wavelength of the pulse.

In accordance with a still further aspect, the invention provides a method
5 of heat treating a treatment layer of an article comprising said treatment
layer and one or more further layers, the method comprising the steps of
illuminating the article with a laser pulse which is temporally extended by
a temporal extender, the temporal width and fluence of the pulse being
selected so that the treatment layer is raised to a treatment temperature T
10 whilst one or more of the further layers are kept substantially below the
treatment temperature T.

Preferably the treatment layer extends from a surface of the article
downwards with one or more further layers below the treatment layer.

15 Alternatively, the treatment layer may be provided between one or more
other layers.

The laser pulse may be temporally extended by partial reflection of a laser
20 beam.

The heat treatment may comprise an annealing process. This may be
adapted to convert the state of some or all of the material of the treated
layer from its untreated state to a different treated state. An example is
25 conversion of a ferroelectric material from a non-perovskite to a
perovskite state, or from low grade perovskite to higher grade perovskite.
Other treatments include heating to relieve locked in stresses within the
first layer, or simply, to cause chemical change in the material of the
layer or some other physical change of state.

30

In accordance with a still further aspect, the invention provides apparatus adapted to heat treat a treatment layer of an article comprising a treatment layer and one or more further layers, comprising a laser adapted to produce a laser pulse, a temporal extender adapted to extend the temporal
5 width of the laser pulse, and means for guiding the temporally extended pulse onto the article, whereby the laser pulse is adapted to raise the temperature of that treatment layer above a treatment temperature T whilst one or more of the further layers are kept substantially below the treatment temperature.

10

Looked at one way, an object of the invention in at least one arrangement is to provide a more uniform heating of the material in the first layer than can be achieved using standard commercially available laser devices. Alternatively, it may be considered as a technique and apparatus for
15 reducing the surface temperature at the first layer by providing the required heat energy at a slower rate using an extended pulse. In another aspect, the invention ensures that heat is kept away from unwanted areas of the article by providing a suitable duration extended pulse which provides more controlled and accurate heating of the layer of material.

20

There will now be described, by way of example only, one embodiment of the present invention with reference to the accompanying drawings of which:

25

Figure 1 is a representation of an integrated uncooled IR detector having a ferroelectric layer that is annealed in accordance with the present invention;

30

Figure 2 is a plot of the measured temporal pulse shape for a prior art Lambda Physik LPX210i excimer laser;

Figure 3 illustrates a set of predicted prior art temperature profiles through 1 micron of PST due to exposure to a non-extended laser pulse of width 25nm and a fluence of $0.1\text{J}/\text{cm}^2$;

5

Figure 4 illustrates the predicted prior art temperature profiles through the depth of the PST layer stack during and after exposure to a non-extended laser pulse with fluence $0.1\text{J}/\text{cm}^2$;

10

Figure 5 illustrates the measured temporal pulse shape for a x 10 pulse-extension action with a 35ns interpulse delay using an excimer laser as in accordance with the present invention;

15

Figure 6 shows predicted temperature profiles for a 1 micron layer of PST when exposed to an extended laser pulse with fluence $0.2\text{J}/\text{cm}^2$;

20

Figure 7 shows predicted temperature profiles through the depth of the PST layer stack during and after exposure to an extended laser pulse with fluence $0.2\text{J}/\text{cm}^2$;

25

Figure 8 is an XRD θ - 2θ scan of as deposited, unannealed, layer of sol-gel PZT;

Figure 9 is an XRD θ - 2θ scan of pulse-extended laser annealed sol-gel PZT; and

30

Figure 10 is a cross-section illustrating a typical microbridge device annealed in accordance with the invention; and

Figure 11 illustrates an apparatus of one embodiment of the invention.

In order to quantify the improvements that are attainable using a method and apparatus in accordance with the present invention a model of the temperature profiles throughout a typical infra-red sensing device was developed and experimental data was obtained using both a prior art single pulse laser and a pulse-extended laser in accordance with the invention.

The diffusion equation for heating of an arbitrary material using laser radiation has been solved. Assuming the layer stack and the irradiating laser beam to be uniform in the x-y plane, the later due to the use of a beam homogeniser, then the equation can be expressed in a uni-dimensional form as:

$$\frac{\partial T}{\partial t} = \frac{\alpha}{\rho c_p} I(z,t) + \sigma \epsilon (T^4 - T_a^4) + \frac{1}{\rho c_p} \frac{\partial}{\partial z} \left(k \frac{\partial T}{\partial z} \right)$$

where $I(z,t)$ is the power density of the laser light at a depth z and at a time t , T is the temperature in the absorbing medium, T_a is the ambient temperature of the annealing chamber and ϵ, ρ, Cp, k and α are the emissivity, density, specific heat, thermal conductivity and absorption coefficient respectively. By inputting the thermal properties of a ferroelectric material such as PST and the temporal form of the laser pulse applied to the material, the temperature distribution throughout the material can be estimated. It should, however, be noted that the effect of latent heat due to the formation of the perovskite phase was not taken into account, but this does not effect the invention.

The structure of a typical sensor is shown in cross section in Figure 10 and in plan in Figure 1 of the accompanying drawings (and in Table 1 on page 3)

- 5 The sensor comprises a microbridge 10 as shown in Figure 1 which has a main detector area 12 which forms a single pixel in an array of pixels in an imaging device. The main detector area 12 comprises a bridge 14 of sensing material that is responsive to incident radiation, typically a ferroelectric material such as Lead Scandium Tantalate. The microbridge
- 10 area is typically 50 microns by 50 microns. The leg width is around 5 microns, and the length of the leg around 30 microns. The sensing sensing material is provided with electrically conducting coatings on both the lower and upper faces. The coating on the upper face is patterned to be only on the upper bridge body area. The coating on the lower face is
- 15 patterned in the same shape as the sensing material, and thus is continuous down the microbridge legs. An electrical discontinuity (not shown) in the lower coating separates the bridge 14 into two areas. A substrate or base layer (not shown) of silicon is provided, and the bridge 14 is supported away from the silicon base layer by a pair of legs 18 which slope down to
- 20 contact the main detector area 12 at diagonally-opposed corners 20 and 22. Feet 24 and 26 of the legs contact the silicon base layer.

- Figure 10 shows a cross-section view of the microbridge in Figure 2 taken so as to pass through the two feet of the microbridge (not to scale). In
- 25 the arrangement of Figure 10, there is a silicon base layer or substrate A which has a depth of, typically, 300 to 500 microns, a silicon dioxide insulating layer B extending above the silicon layer with a depth of about 0.5 microns, a space G (filled with sacrificial material during the manufacture of the device) with an average depth of 1 to 2 microns, a
- 30 titanium layer and a platinum layer D (titanium of the order of 100Å,

platinum layer of the order of $1,000\text{\AA}$), a ferroelectric layer E, in this example of Lead Scandium Tantalate having a depth/thickness of about 1 micron, and a titanium layer F having a depth of about 100 to 200\AA .

- 5 The upper and lower surfaces of the ferroelectric material have metallic coatings. The metal coating on the lower surface comprises a platinum layer overlaying in titanium layer. The metal coating on the upper surface comprises a titanium layer. The distance between the conducting layers D and F of Figure 2, referenced H, when combined with the
- 10 refractive index of the sensing material, results in an optical path equal to one quarter of the wavelength of the radiation to which the microbridge is to be sensitive. Thus, the microbridge is tuned for absorption to a particular wavelength by the thickness of the thermally sensitive layer. For 10 micron wavelength radiation this translates to around 1 micron
- 15 physical thickness for most ferroelectric ceramic materials, and preferably for lead scandium tantalate. For optimum absorption, the titanium coating on the upper surface has a sheet resistance matched to free-space i.e. $377\text{ohms per square}$, and the platinum coating on the lower surface has a high infra-red reflectance i.e. is over 100 nanometers thickness. The
- 20 lower coating has a discontinuity, I, (to give, effectively, two parallel-plate capacitor plates connected back-to-back in series). This allows the lower coating to be used to give two contacts to the sensing material and allows the upper coating to be electrically floating.
- 25 Conductive metal interconnect tracks, C, are provided on the silicon base layer, to connect the signals from the microbridge to the read-out electrodes. The silicon base layer is coated with an insulating layer, B, in this case silicon dioxide, to electrically insulate the interconnect tracks. The two halves of the lower coating on the sensing material are connected
- 30 each to a separate interconnected track on the silicon base layer. In an

imaging device where readout electronics are in the base silicon layer, then the interconnect tracks will be patterned with the read-out electronics i.e. under the silicon dioxide or other similar passivation layer, B, and the contacts to the microbridge will be through a via in the passivation layer.

5

Shown in Figures 3 and 4 are the temperature distributions throughout the layers of the device of Figure 10 due to a 25cm wide laser pulse from an excimer laser with a fluence of $0.1\text{J}/\text{cm}^2$. The temperature distribution was modelled in simulation by ten discrete $0.1\mu\text{m}$ thick layers, the mean temperatures of which are shown as a function of depth in Figure 4. The surface temperature is very high as well as the temperature gradient through the PST layer. Figure 4 shows the temperature distribution with distance from the irradiated surface. Again, high temperature gradients and excessive surface heating are apparent.

15

In order to solve the problem of the high surface temperature, an apparatus in accordance with an aspect of the invention has been proposed. This is illustrated in Figure 11. It comprises a commercial excimer laser 100 controlled by a computer 101 which produces a light pulse with a pulse duration at full width at half maximum (fwhm) of 25ns. The output of the laser 100 is passed through a variable attenuator 150 and a temporal extender 200 which effectively increases the pulse duration and hence the diffusion length. The extended laser pulse is then passed through an anamorphic telescope 210 and a beam homogeniser 220 to a vacuum chamber 300.

25

The vacuum chamber 300 incorporates a heating plate 301 upon which a wafer 302 containing an unprocessed sensor is placed behind a UV transparent window 303. The heat raises the ambient temperature of the wafer defining the sensor, and the pulsed laser beam anneals the PST

30

layer into a layer in the perovskite phase. A vacuum pump 304 is provided for evacuating the chamber whilst an inlet 305 allows a process gas to be introduced to the chamber.

- 5 A measured temporal profile from the apparatus is shown in Figure 5. Ten sub-pulses can be seen corresponding to the main pulse extension action and then decaying subsidiary pulses which correspond to imperfections in the system. The pulse extender has increased the pulse duration from a full width at half maximum of 25ns to one of 350ns.

10

Assuming the pulse to have a fluence of $0.1\text{J}/\text{cm}^2$ (as for the unextended pulse) the effect of the pulse on the layers is shown in Figure 6 for the mean layer temperatures and in Figure 7 for the temperature distribution with distance from the irradiated surface at various times. Again, a $1\mu\text{m}$

- 15 layer of PST modelled as ten $0.1\mu\text{m}$ layers was assumed.

The effect of the pulse extender is to effectively slow down the rate of energy delivered to the surface thereby giving the heat generated more time to diffuse away. The surface temperatures are correspondingly lower
20 than for the non-extended pulse with lower thermal gradients, and hence an average higher temperatures, within the whole of the PST layer. Clearly, the pulse length is still sufficiently short to prevent the temperature of the topmost surface of the silicon wafer - where the active circuitry resides - from rising more than a few degrees above the ambient
25 temperature of the wafer as a whole.

Initial studies using apparatus based on the arrangement shown in Figure 11 with sol-gel deposited PZT have shown that the technique is capable of crystallising amorphous as-deposited material into the required
30 ferroelectric perovskite phase without damaging the underlying ROIC.

Figure 8 shows an x-ray diffraction (XRD) θ -2 θ scan of the material as deposited. The reflections present which are visible in the Figure are due to the platinum underlining the PZT and an intermetallic which forms due to a reaction between the platinum in the substrate and the lead in the as-deposited PZT.

Figure 9 shows a similar XRD θ -2 θ scan of the pulse-extended excimer laser annealed material with 10^4 pulses at a fluence of $80\text{mJ}/\text{cm}^2$ with the substrate heated to an ambient temperature at 300°C in an oxygen filled chamber. Reflections from perovskite PZT can be clearly observed showing that the material has been crystallised into the correct phase.

25

CLAIMS

1. A method of producing an integrated ferroelectric device (10) comprising a first layer of material capable of existing in a ferroelectric state and a second layer of material defining an integrated circuit, the method comprising the steps of:

producing (100) a pulse of energy having a first temporal width;

- 10 extending the temporal width of said pulse by passing it through a temporal extender (200) to produce a processed pulse having a greater temporal width; and

- 15 illuminating the first layer with said processed pulse to convert some or all of the material in the first layer from a non-ferroelectric state into a phase capable of exhibiting ferroelectricity or otherwise improving the quality of the material of the first layer without exceeding the temperature budget of the integrated circuit of said second layer.

- 20 2. A method according to claim 1 which further comprises generating a number of such processed pulses and sequentially illuminating the device (10) with said pulses.

- 25 3. A method according to claim 1 or claim 2 in which the material of the first layer comprises a low grade deposited perovskite and the method improves the quality of the perovskite material.

- 30 4. A method according to claim 1 or claim 2 in which the first layer comprises material deposited substantially in a non-perovskite phase and the method converts some or all of the material into the perovskite phase.

5. A method according to any preceding claim in which the pulse of energy comprises a pulse of energy produced using a laser (100).
6. A method according to claim 5 in which the pulse produced by the laser (100) has a temporal length of substantially 10ns, or 20ns or perhaps substantially 25ns, or any value within a range of values limited by one or more of the preceding values.
7. A method according to any preceding claim in which the temporal extender increases the temporal length of the pulse to produce a processed pulse with a temporal length of approximately 300ns, or between substantially 300ns and 400ns.
8. A method according to any preceding claim in which the processed pulse comprises more than one sub-pulse, each sub-pulse corresponding to a pulse action of the extender (200).
9. A method according to any preceding claim in which the processed pulse has a fluence and temporal width that is compatible with the properties of the material of the first layer such that the temperature throughout the layer (or over a substantial depth of the first layer) exceeds a predetermined anneal temperature whilst the temperature of the second layer is within the temperature budget of the circuitry.
10. A method according to any preceding claim in which the first layer comprises the top layer of the device (10).
11. A method according to any preceding claim in which two different sources of energy are produced, each source producing a respective pulse and at least one of said respective pulses being extended by a pulse

extender (200) to produce a processed pulse, and in which the first layer is illuminated by both pulses.

12. A method according to claim 11 in which the layer is illuminated
5 by both pulses substantially simultaneously.

13. A method according to any one of claims 11 or 12 which further
comprises providing a metallic layer between the first layer and the
second layer and illuminating the first layer with the two different
10 processed pulses.

14. A method according to any preceding claim in which the first layer
is illuminated with the processed pulse whilst the ambient temperature of
the device is maintained higher than room temperature.
15

15. An apparatus for producing an integrated ferroelectric device (10),
said device comprising at least a first layer of material capable of existing
in a perovskite phase and second layer of material defining an integrated
circuit, the apparatus comprising:
20

pulse generating means (100) adapted to generate a pulse of energy having
a first temporal width;

pulse extending means (200) adapted to extend the temporal pulse width of
25 said pulse to provide a processed pulse of greater temporal width;

and guide means (210, 220) adapted to guide said processed pulse of
energy onto said first layer.

16. Apparatus according to claim 15 which further comprises depositing means for depositing said first layer of material above said second layer in which some or all of said first layer is in a non-perovskite phase.

5

17. Apparatus according to claim 15 or claim 16 in which the pulse generating means comprises a laser (100).

10

18. Apparatus according to claim 17 in which the laser (100) has a wavelength in the ultraviolet spectrum.

15

19. Apparatus according to any one of claims 15 to 17 in which the depositing means is adapted to deposit a first layer of material above the second layer after one or more intermediate layers are deposited onto the second layer.

20. Apparatus according to claim 19 in which one of the intermediate layers comprises a sacrificial layer.

20

21. Apparatus according to any one of claims 15 to 20 in which the pulse extender is adapted to increase the temporal pulse width of the first pulse by substantially two times or four times, or substantially ten times or more than ten times or any value therebetween.

25

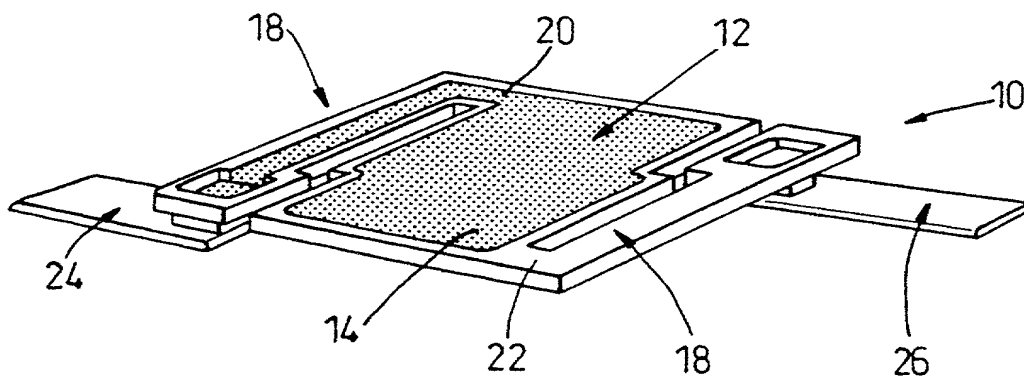
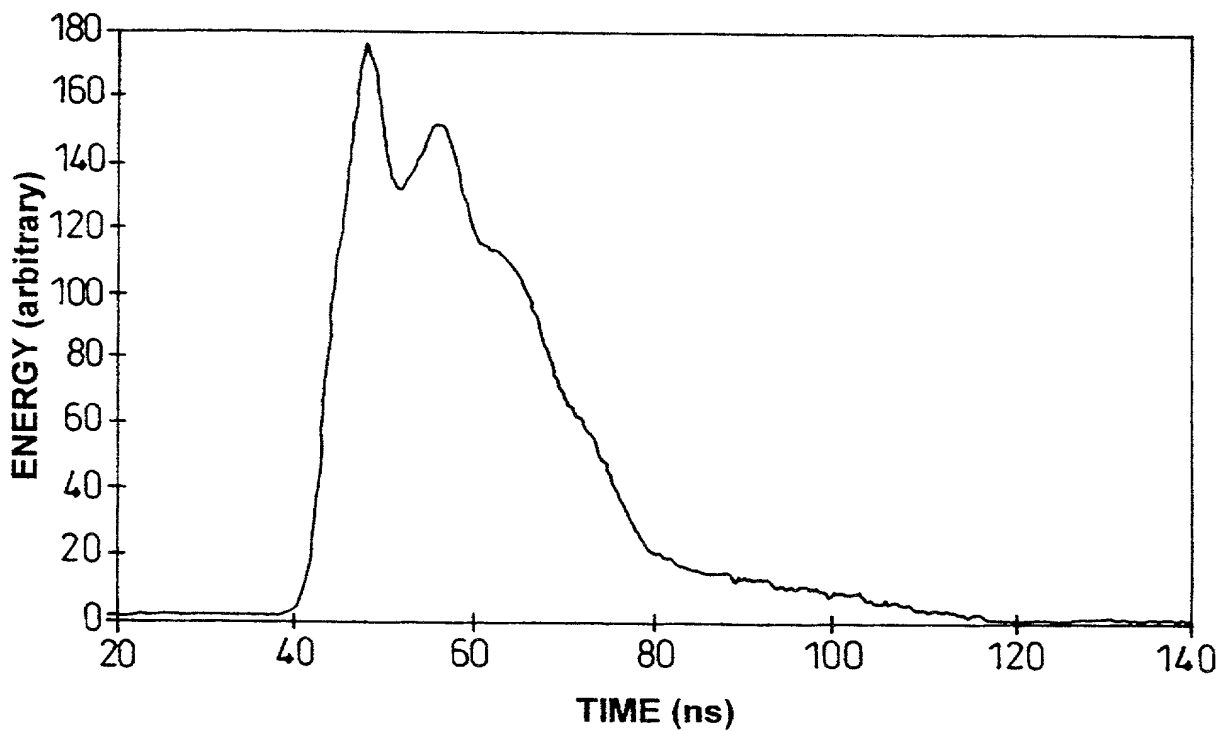
22. Apparatus according to any one of claims 15 to 21 in which the pulse extender is adapted to produce a processed pulse that comprises a number of sub-pulses.

30

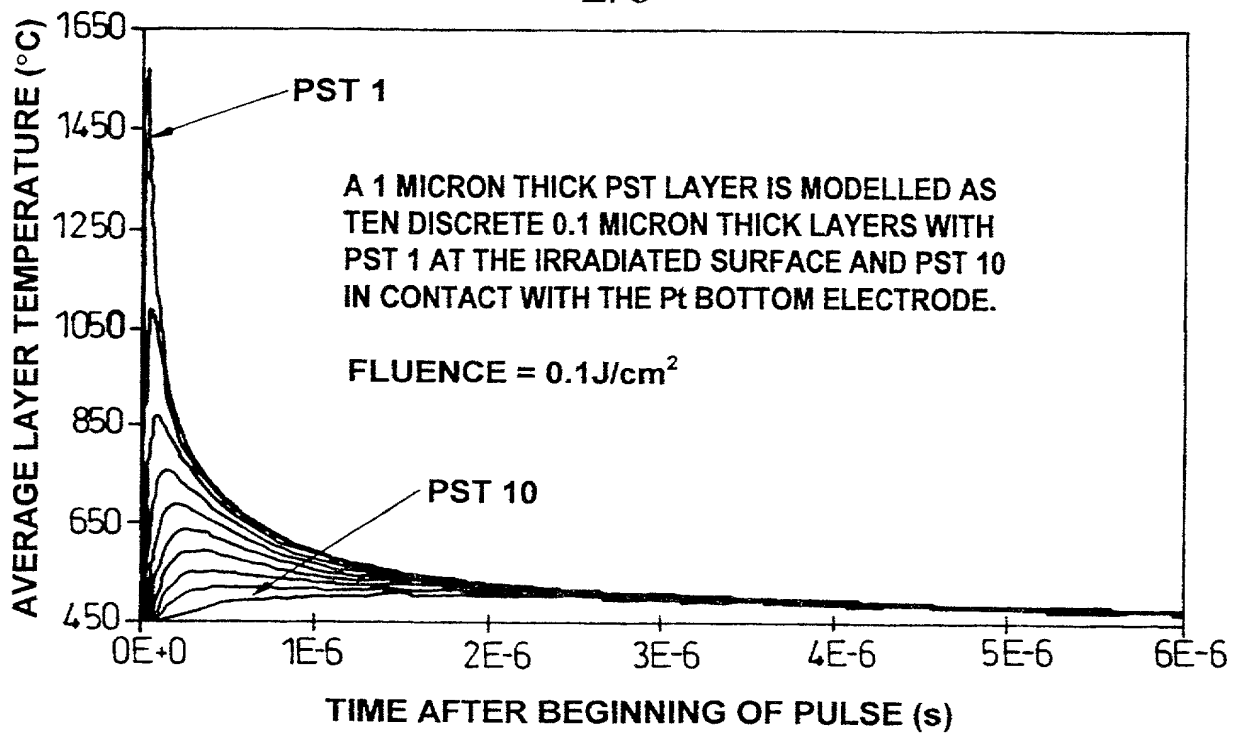
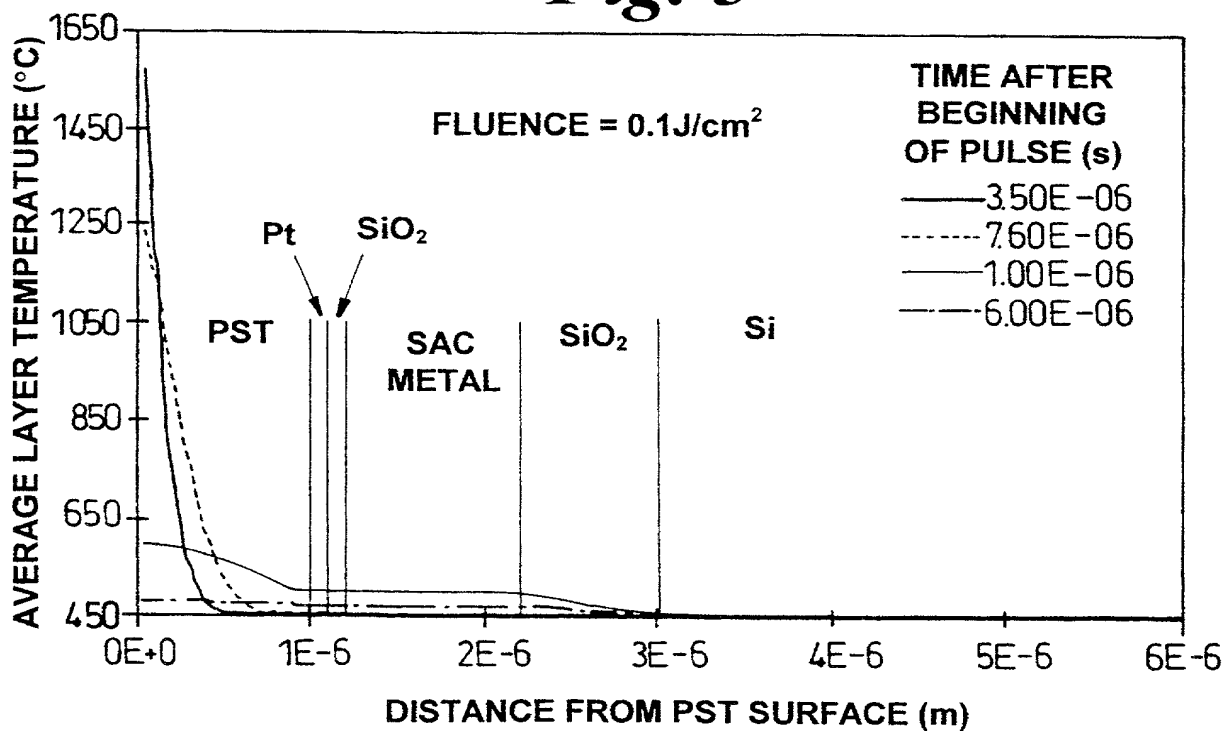
23. An integrated ferroelectric device comprising at least a first layer of ferroelectric material and a second layer comprising an integrated

AMENDED SHEET

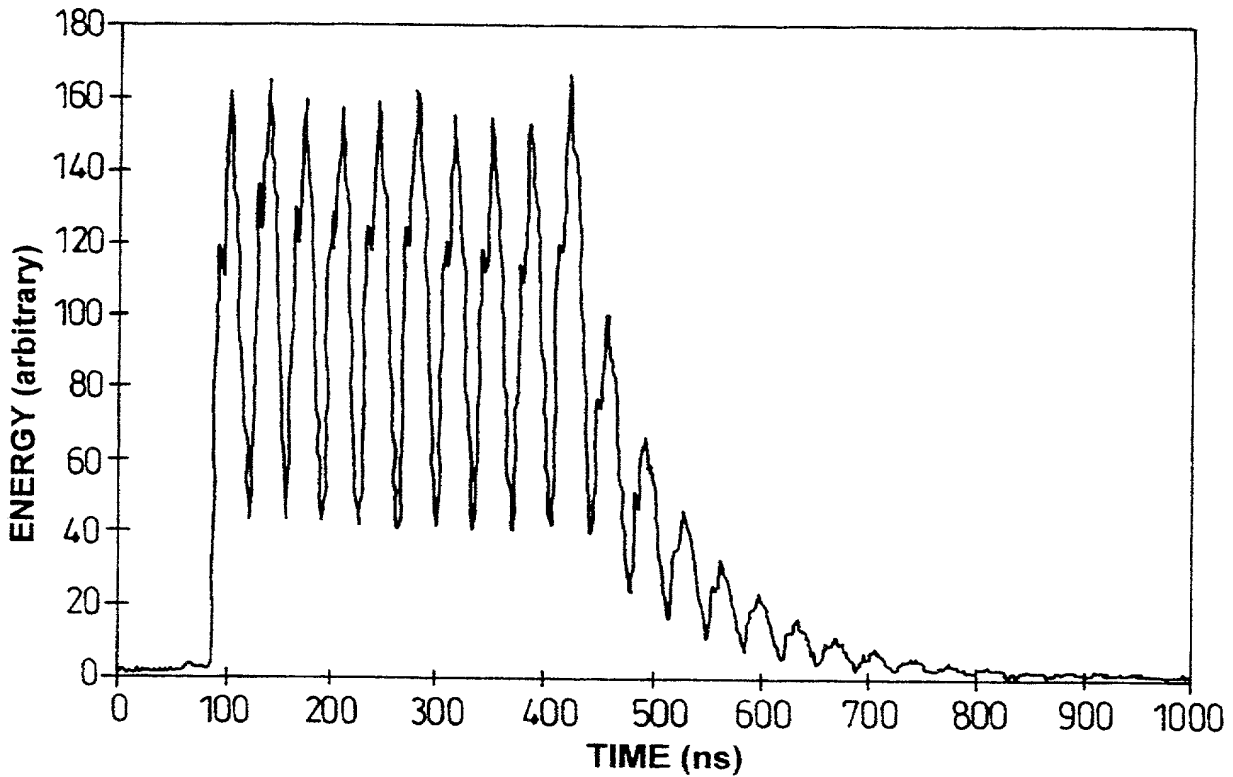
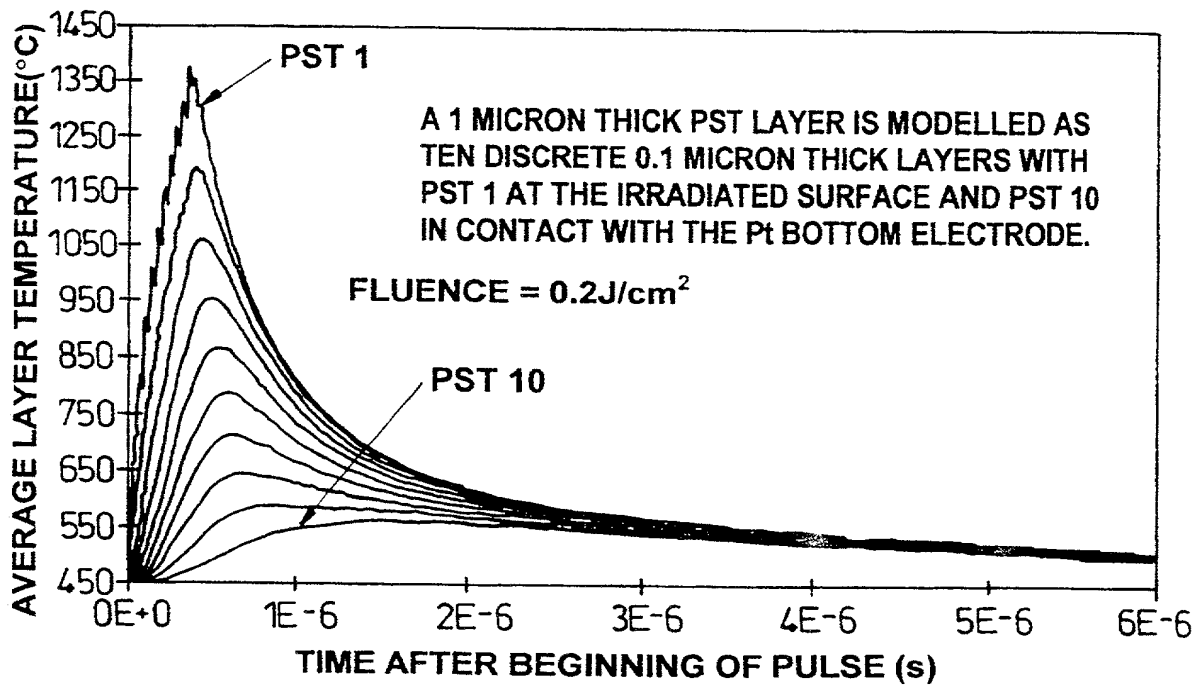
1/6

*Fig. 1**Fig. 2*

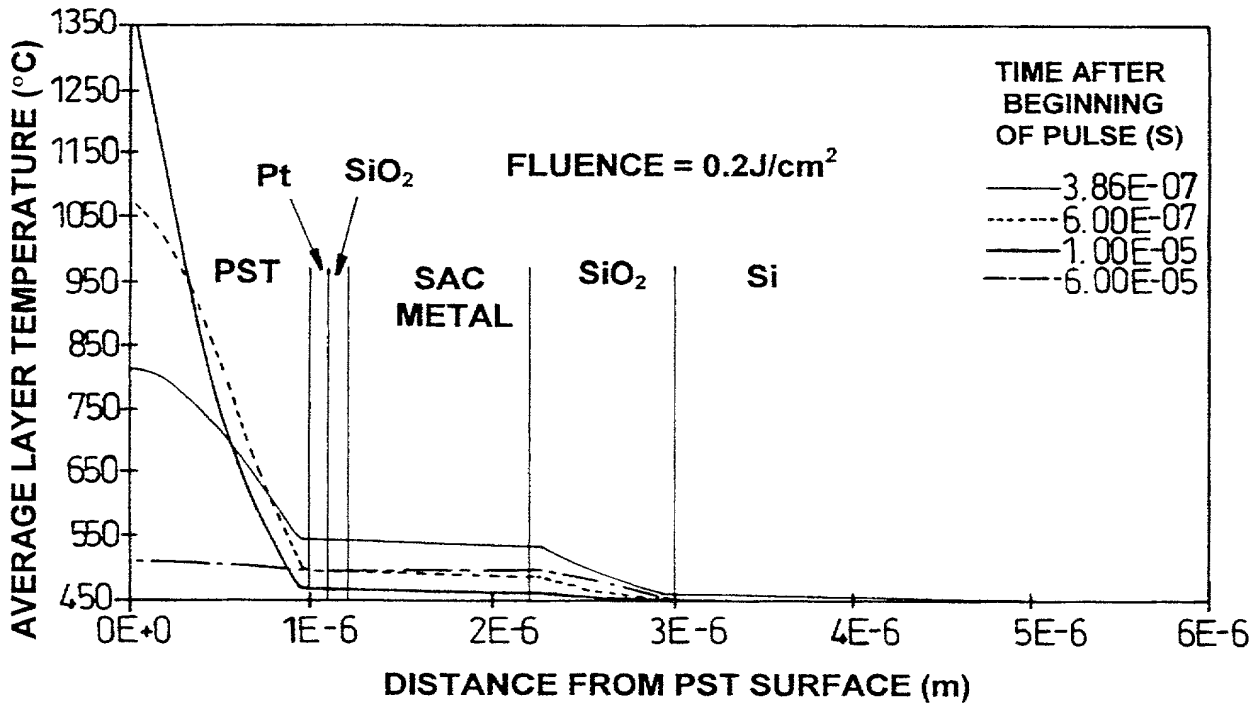
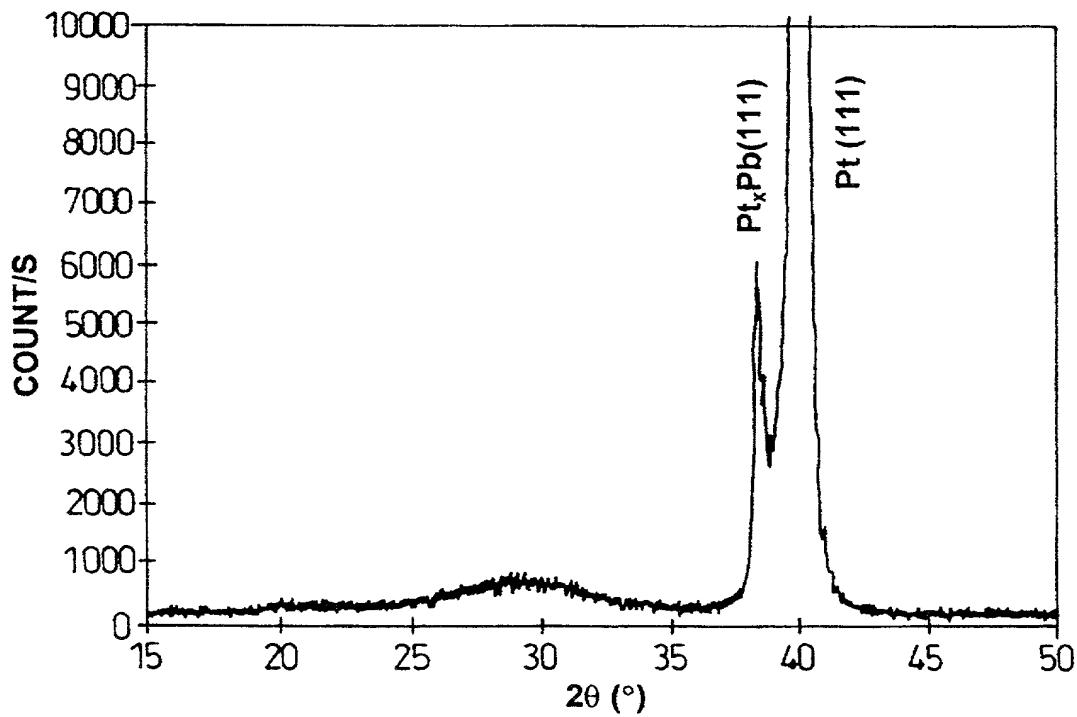
2/6

*Fig. 3**Fig. 4*

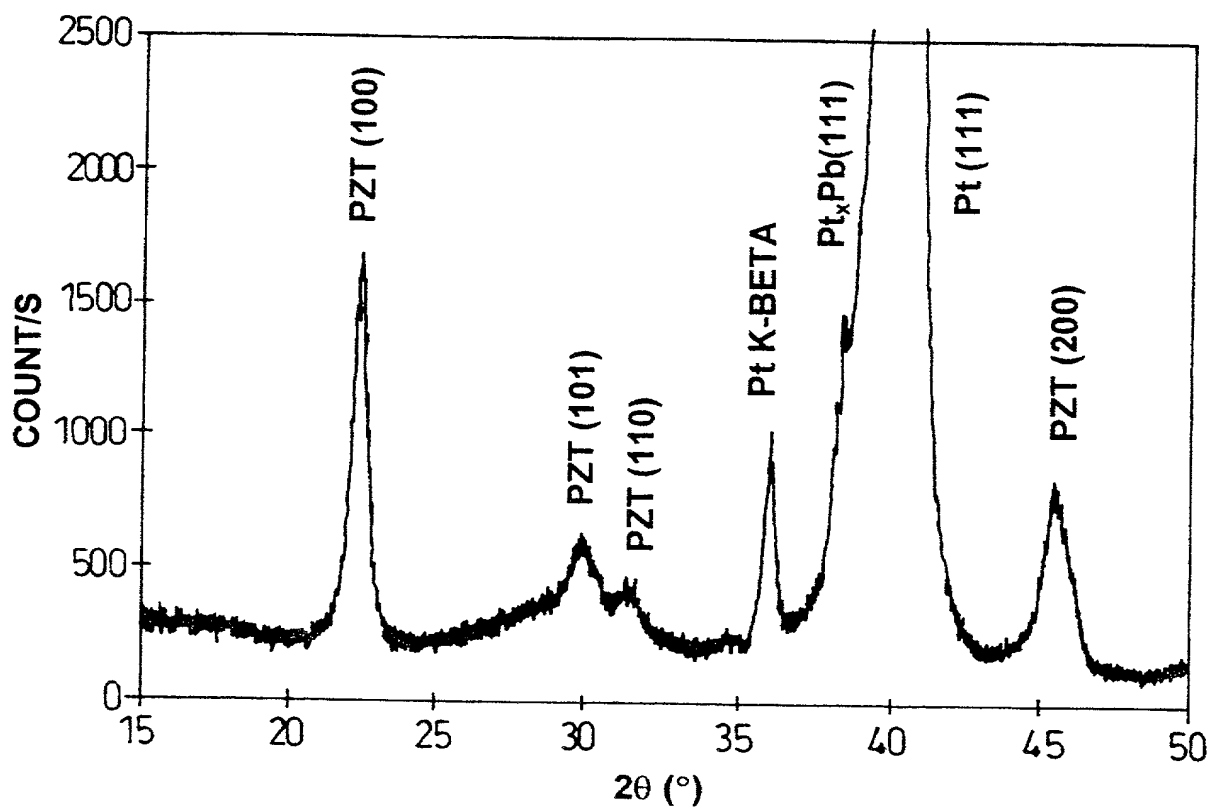
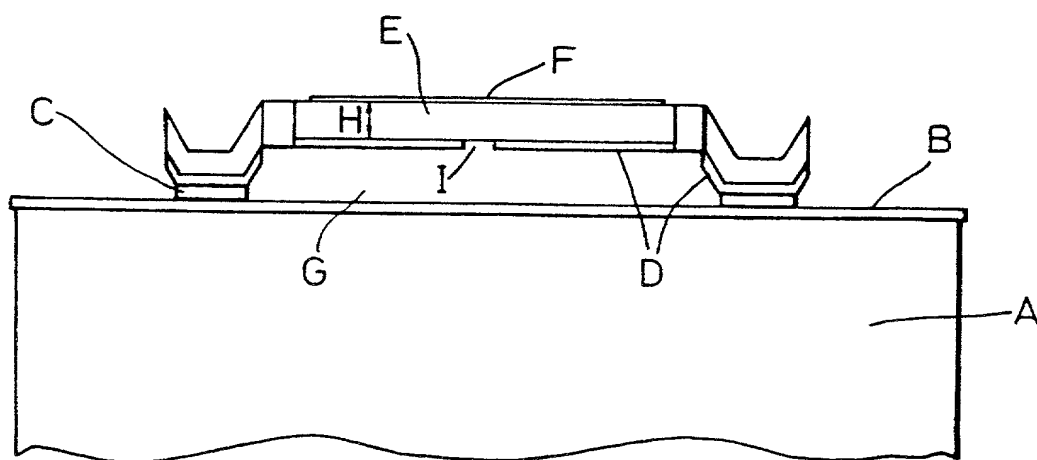
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*Fig. 5**Fig. 6*

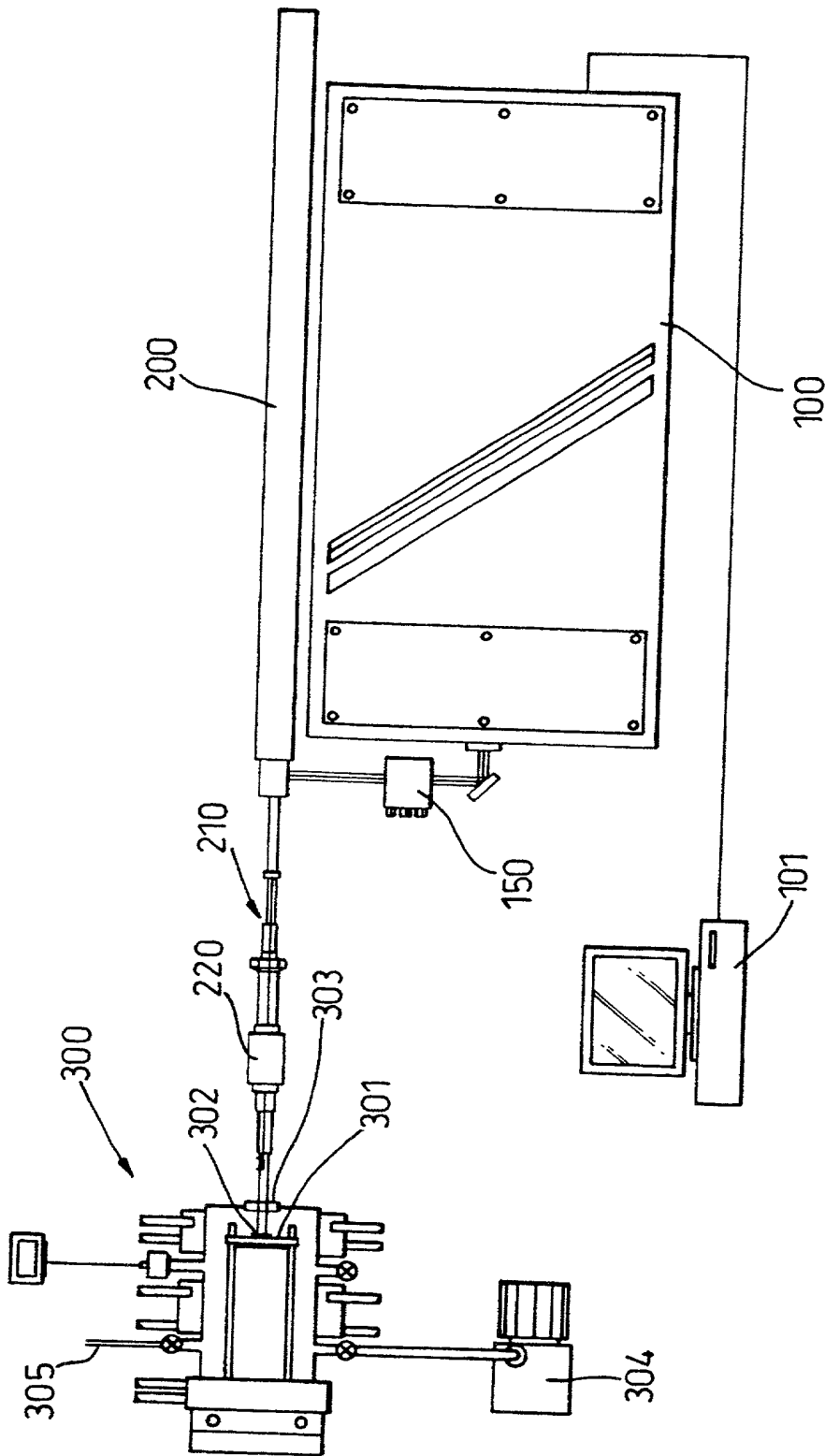
4/6

*Fig. 7**Fig. 8*

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*Fig. 9**Fig. 10*

6/6

*Fig. 11*

RULE 63 (37 C.F.R. 1.63)
INVENTORS DECLARATION FOR PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

As a below named inventor, I hereby declare that my residence, mailing address and citizenship are as stated below next to my name, and I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

IMPROVEMENTS RELATING TO ANNEALING

the specification of which (check applicable box(es)):

☐ is attached hereto
☐ was filed on _____ as U.S. Application Serial No. _____ (Atty Dkt. No. 124-884)
☒ was filed as PCT International application No. PCT/GB/00/00753 on 3 March 2000
and (if applicable to U.S. or PCT application) was amended on 27 February 2001

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose to the Patent Office all information known to me to be material to patentability as defined in 37 C.F.R. 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed or, if no priority is claimed, before the filing date of this application:

Priority Foreign Application(s):

Application Number	Country	Day/Month/Year Filed
9805098 1	Great Britain	6 March 1999

I hereby claim the benefit under 35 U.S.C. §119(e) of any United States provisional application(s) listed below

Application Number	Date/Month/Year Filed
--------------------	-----------------------

I hereby claim the benefit under 35 U.S.C. 120/365 of all prior United States and PCT international applications listed above or below

Prior U.S./PCT Application(s):

Application Serial No.	Day/Month/Year Filed	Status: patented pending, abandoned
PCT/GB/00/00753	3 March 2000	

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon. And on behalf of the owner(s) hereof, I hereby appoint **NIXON & VANDERHYE P.C., 1100 North Glebe Rd., 8th Floor, Arlington, VA 22201-4714, telephone number (703) 816-4000** (to whom all communications are to be directed), and the following attorneys thereof (of the same address) individually and collectively owner's/owners' attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith and with the resulting patent: Larry S. Nixon, 25640; Arthur R. Crawford, 25327; James T. Hosmer, 30184; Robert W. Faris, 31352; Richard G. Besho, 22770; Mark E. Nussbaum, 32348; Michael J. Keenan, 32100; Bryan H. Davidson, 30251; Stanley C. Spooner, 27393; Leonard C. Mitchard, 29009; Duane M. Byers, 33383; Jeffrey H. Nelson, 30481; John R. Lastova, 33149; H. Warren Burnard, Jr., 29386; Mary J. Wilson, 32955; J. Scott Davidson, 33489; Alan M. Kagan, 36178; Robert A. Molan, 29834; B. J. Sadoff, 36663; James D. Berquist, 34776; Undeep S. Gill, 37334; Michael J. Shea, 34725; Donald L. Jackson, 41090; Michelle N. Lester, 32931; Frank P. Presta, 19828; Joseph S. Presta, 35329; Joseph A. Rhee, 37515; Raymond Y. Mah, 41426; Chris Comuntzis, 31037; Gary T. Tanigawa, 43180. I also authorize Nixon & Vanderhye to delete any attorney names/numbers no longer with the firm and to act and rely solely on instructions directly communicated from the person, assignee, attorney, firm, or other organization sending instructions to Nixon & Vanderhye on behalf of the owner(s).

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☐ See attached sheet(s) for additional inventor(s) information!!